

PhD studentship (Full-time)

Institution	Xi'an Jiaotong-Liverpool University, China
School	School of Advanced Technology.....
Supervisors	<p><i>Please list all the names in the supervisory team. It should be consistent with the information on your approved PGRS proposal.</i></p> <p>Principal supervisor: Professor/Dr... Wen Liu ... (XJTLU) Co-supervisor: Professor/Dr... Jiangmin Gu and Jie Zhang (School of Chips).....(XJTLU) Co-supervisor: Professor/Dr... Van Zalinge, Harm(UoL)</p>
Application Deadline	Open until the position is filled
Funding Availability	Funded PhD project (world-wide students)
Project Title	High efficiency monolithically integrated GaN HEMTs driver- IC design and Fabrication 高效单片集成 GaN HEMT 驱动 IC 设计与制造
Contact	<p>Please email...Wen.liu...@xjtlu.edu.cn (XJTLU principal supervisor's email address) with a subject line of the PhD project title.</p> <p>The principal supervisor's profile is linked here: https://scholar.xjtlu.edu.cn/en/persons/WenLiu https://scholar.google.com/citations?hl=en&user=Lg7THRoAAAAJ</p>

Requirements:

The candidate should have a first class or upper second class honours degree, or a master's degree (or equivalent qualification), in EST, Physics, EE etc.....

Evidence of good spoken and written English is essential. The candidate should have an IELTS score of 6.5 or above, if the first language is not English. This position is open to all qualified candidates irrespective of nationality.

Degree:

The student will be awarded a PhD degree from the University of Liverpool (UK) upon successful completion of the program.

Funding:

The PhD studentship is available for three years subject to satisfactory progress by the student. The award covers tuition fees for three years (currently equivalent to RMB 99,000 per annum). It also provides up to RMB 16,500 to allow participation at international conferences during the period of the award. The scholarship holders are expected to conduct the majority of their

research at XJTLU in Suzhou, China. However, they may apply for a short-term research visit to the University of Liverpool if the project requires it.

Project Description:

..... Power ICs are ubiquitous for power electronics applications like solar inverters, data centers and motor drives etc. This study will implement a monolithic integrated GaN on Si DC-DC converter, in which the bootstrap gate driver, PWM control circuits and power device are all planed on a single chip. AlGaN barrier digital etching technology will be applied to increase the threshold voltage of D-mode voltage. Furthermore, atomic layer growth will be investigated to improve the breakdown gate-source voltage of the device.....

For more information about doctoral scholarship and PhD programme at Xi'an Jiaotong-Liverpool University (XJTLU), please visit

<https://www.xjtlu.edu.cn/en/admissions/global/entry-requirements/>

<https://www.xjtlu.edu.cn/en/admissions/global/fees-and-scholarship>

How to Apply:

Interested applicants are advised to email... Wen.liu.....@xjtlu.edu.cn (XJTLU principal supervisor's email address) the following documents for initial review and assessment (please put the project title in the subject line).

- CV
- Two formal reference letters
- Personal statement outlining your interest in the position
- Certificates of English language qualifications (IELTS or equivalent)
- Full academic transcripts in both Chinese and English (for international students, only the English version is required)
- Verified certificates of education qualifications in both Chinese and English (for international students, only the English version is required)
- PDF copy of Master Degree dissertation (or an equivalent writing sample) and examiners reports available